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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **SUGATANI, Shinji, et al.**

Group Art Unit: **2814**

Serial No.: **10/634,839**

Examiner: **Howard Weiss**

Filed: **August 6, 2003**

**P.T.O. Confirmation No.: 8605**

**FOR: MANUFACTURING METHOD OF SEMICONDUCTOR DEVICE AND  
SEMICONDUCTOR CHIP USING SOI SUBSTRATE**

**RESPONSE UNDER 37 CFR §1.116**  
**- EXPEDITED RESPONSE -**  
**GROUP ART UNIT 2814**

**MAILSTOP AF**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

January 24, 2005

Sir:

In response to the Office Action dated **September 22, 2004**, extended to **January 22, 2005** by a one- month Petition for Extension of Time, entry of the following amendments and remarks, further examination, and reconsideration are respectfully requested.

**Remarks/Arguments** begin on page 2 of this paper.